

**COMBINED DECLARATION / POWER OF ATTORNEY**

AS BELOW NAMED INVENTOR, I HEREBY DECLARE THAT: This Declaration is of the following type:

- ☒ Original                      ☐ Supplemental                      ☐ Continuation-In-Part                      ☐ Divisional  
☐ Continuation                      ☐ National Stage of PCT

My residence, post office address and citizenship are as stated below next to my name: I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention **TIERED BUILT-IN SELF-TEST (BIST) ARCHITECTURE FOR TESTING DISTRIBUTED MEMORY MODULES** the specification of which:

- ☐ is attached hereto.  
☒ was filed on July 29, 2003 as Serial No. 10/630,516  
☐ was amended on \_\_\_\_\_ (if applicable).  
☐ was described and claimed in PCT International Application No. \_\_\_\_\_ filed on \_\_\_\_\_ and as amended under PCT Article 19 on \_\_\_\_\_.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above. I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, Sec. 1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, Sec. 119 of any foreign application(s) for patent or inventor's certificate or of any PCT International application(s) designating at least one country other than the United States of America listed below and have also identified below any foreign application(s) for patent or inventor's certificate or any PCT International application(s) designating at least one country other than the United States of America filed by me on the same subject matter having a filing date before that of the application(s) of which priority is claimed.

## Priority Claimed

(Country)	(Application No.)	(Day/Month/Year/Filed)	(Yes)	(No)
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I hereby claim the benefit under Title 35 USC 119(e) of any United States provisional application(s) listed below:

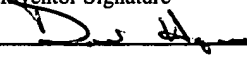
(Serial No.)	(Filing Date)
60/456,452	March 20, 2003

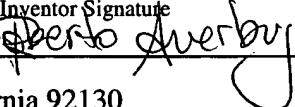
I hereby claim the benefit under Title 35 USC 120 of the United States application(s) listed below, and insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35 USC 112, I acknowledge the duty to disclose material information as defined in Title 37 CFR 1.56(a) which occurred between the filing date of the prior application and the national or PCT International filing date of this application:

(Serial No.)	(Filing Date)	(Status)
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I hereby appoint the attorneys and/or agents associated with Customer No. 23696 to prosecute this application and to transact all business in the U.S. Patent and Trademark Office connected therewith. Please direct all telephone calls to Philip R. Wadsworth at (858) 651-4404 and address all correspondence to: QUALCOMM Incorporated, Patent Department, 5775 Morehouse Drive, San Diego, California 92121-1714.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full Name of First or Joint Inventor David W. Hansquine	Inventor Signature 	Date March 1 / 2004
Residence 12885 Sundance Ave., San Diego, California 92129	Citizenship Canada	
Post Office Address 12885 Sundance Ave., San Diego, California 92129		

Full Name of First or Joint Inventor Roberto F. Averbuj	Inventor Signature 	Date February 26 / 2004
Residence 4110 Via Candidiz, #103, San Diego, California 92130	Citizenship United States of America	
Post Office Address 4110 Via Candidiz, #103, San Diego, California 92130		

## ASSIGNMENT

TO WHOM IT MAY CONCERN:

For the sum of One Dollar and other valuable consideration to us in hand paid, receipt of which is hereby acknowledged, be it known that we **David W. Hansquine**, a citizen of Canada, having a place of residence at 12885 Sundance Ave., San Diego, California 92129, and **Roberto F. Averbuj**, a citizen of the United States of America, having a place of residence at 4110 Via Candidiz, #103, San Diego, California 92130; have sold, assigned and transferred and by these presents do sell, assign, transfer and set over unto **QUALCOMM Incorporated**, a Delaware corporation, having a place of business at 5775 Morehouse Drive, San Diego, California 92121-1714, its successors, legal representatives, or assigns, the whole right, title and interest in and to a certain invention relating to:

### **TIERED BUILT-IN SELF-TEST (BIST) ARCHITECTURE FOR TESTING**

#### **DISTRIBUTED MEMORY MODULES**

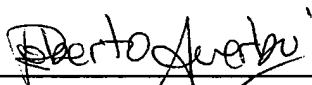
by us devised and the application for United States Patent therefore filed in the United States Patent and Trademark Office on **July 29, 2003**, and assigned Serial No. **10/630,516**, including, without limitation, all provisional patent applications, **including Serial No. 60/456,452, filed March 20, 2003**, and all original and reissue patents granted thereof, and all divisions, and continuations thereof, including the subject matter of any and all claims which may be obtained in every such patent, and all foreign rights to said invention, and covenant that we have full right to do so, and agree that we will communicate to said corporation or its representatives all facts known to us respecting said invention, whenever requested, and testify in any legal proceedings, sign all lawful papers, make all rightful oaths and generally do everything possible to aid said corporation, its successors, assigns and nominees, to obtain and enforce proper patent protection for said invention in all countries.

The Commissioner of Patents and Trademarks is requested to issue the Letters Patent which may be granted for said invention or any part thereof unto said corporation in keeping with this Assignment.

Done at San Diego, California this 1 day of March, 2004.

  
\_\_\_\_\_  
David W. Hansquine

Done at San Diego, California this 26<sup>th</sup> day of February, 2004.

  
\_\_\_\_\_  
Roberto F. Averbuj